

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Lawrence Pileggi)
Application No.: 10/707,205) Group Art Unit: 2138
Filed: November 26, 2003) Examiner: Saqib Javaid Siddiqui
Title: SYSTEM AND METHOD TO) Confirmation No.: 1204
TEST INTEGRATED CIRCUITS)
ON A WAFER)

)

Mail Stop: Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 CFR §1.111

Sir:

In response to the Office Action dated as mailed March 13, 2006, please amend the above-identified application as follows:

There are no Amendments to the Specification.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 15 of this paper.

**CERTIFICATE OF
ELECTRONIC FILING**

I hereby certify that this correspondence is being electronically filed with the US Postal Service on June 1, 2006 addressed to Mail Stop: Amendment, Commissioner of Patents, PO Box 1450, Alexandria, VA 22313-1450

Katie M. Efland

Katie M. Efland